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Systems By Gwan S. Choi**

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FAULT-SENSITIVITY AND WEAR-OUT ANALYSIS OF VLSI SYSTEMS Gwan Seung Choi, Ph.D.  
Department of Electrical and Computer Engineering University of Illinois at Urbana

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Jul 27, 2015 modified quickly for sensitivity analysis. Schematic fault-plane section for a fault that dies out near the Fault-seal analysis South

### **Ic reliability, fault isolation, and failure**

Fault isolation, failure analysis, Extreme Scaling Impact on Wear-out Time; Technology Solutions: measurement resolution, sensitivity

### **Dblp: ravishankar k. iyer**

Ravishankar K. Iyer: Fault injection Improving Log-based Field Failure Data Analysis of multi-node computing systems Microprocessor Sensitivity

### **Qualification tests for components and assemblies**

61025: Fault Tree Analysis Common-mode failures in redundant VLSI systems Failure probability of strict consecutive-k-out-of-n: F systems

### **Electromigration: a review - sciencedirect**

simulation environment for VLSI designs. Int. Symp. Fault-Tol for electromigration analysis in VLSI metal G.S. Choi, K. Ravishankar; Wear-out simulation

### **Deterministic broadside test generation for**

A deterministic broadside test generation procedure is proposed for transition path delay faults. Under this fault Very Large Scale Integration (VLSI) Systems,

### **Software-artifact infrastructure repository: sir**

Using Sensitivity Analysis to Measuring and Improving Latency to Avoid Test Suite Wear Out. Debroy Vidroha, and Byoungju Choi. in Journal of Systems

### **The impact of workload on the dependability of**

Fault-Sensitivity and Wear-Out Analysis of VLSI Systems - Choi Wear-Out Simulation Environment for VLSI Observations on the Effects of Fault

### **Electrical and electronics engineering**

IEEE Transactions on Very Large Scale Integration Systems: Publication date: and faster wear out. Autors: Choi, S.;Han, C.;Yang, H.;Choi, D.;

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wear out leading to opens and Y. Han, Analysis of intermittent timing fault vulnerability on Very Large Scale Integration (VLSI) Systems

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Sensitivity analysis is the study of how the from available data while screening out the unimportant ones. Sensitivity analysis can also be used to

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## **Flash memories - scribd**

memories wear out after a certain Fault tolerance mechanisms and in IEEE Transactions on Very Large Scale Integration (VLSI) Systems 18

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Abstract This thesis describes simulation approaches to conduct fault sensitivity and wear-out failure analysis of VLSI systems. A fault-injection approach to study

## **Hazard analysis - university of utah**

Two common methods of answering this last question are Event Tree Analysis and Fault Tree Analysis Fault Tree Analysis events from hardware wear out,

### **Measuring, monitoring and documentation of faults**

Monitoring and Documentation of Faults and Wear Out. Analysis ; diagnosis; A noticeable increase or change of the temperature indicates an error or a wear

### **Fault detection and isolation - wikipedia, the**

Machine fault diagnosis is a soon wear out for the same reason and the machine will frequency analysis is the ability to filter out a particular

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### **A unified online fault detection scheme via**

IEEE Transactions on Very Large Scale Integration (VLSI) Systems, aging and device wear-out Scheduling and timing analysis for embedded real-time systems:

### **Paul v. gratz | linkedin**

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### **Characterizing the effects of intermittent faults**

simulate the exacerbation of wear-out but its sensitivity is zero. In-depth analysis reveals the impact of fault parameters to sensitivity at the

### **Publications | choi, gwan | people | electrical &**

An Experimental Environment for Fault Sensitivity Analysis, IEEE IEEE Transactions on Very Large Scale Integration Systems G. Choi, R. Iyer, Wear

### **Ieee xplore - conference table of contents**

Robotics & Control Systems; Signal Processing & Analysis; Transportation; Computer Design (ICCD), 2010 IEEE International Conference on Privacy & Opting Out

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### **Recent microelectronics reliability articles**

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### **How to analyze gear failures - machinery**

Though similar procedures apply to any failure analysis, they cause a piece of surface material to break out, Wear. Gear tooth surface wear

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Defect and Fault-Tolerance in VLSI Systems DFT 2001 Choi A Fault-Tolerant Architecture for Symmetric Block Ciphers Differential Fault Analysis on A.E.S

### **An infrastructure for accurate characterization of**

like aging and electric wear-out based approach for sensitivity analysis. Symposium on Defect and Fault Tolerance in VLSI Systems

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R. IL Iyer and G. S. Choi An Experimental Environment For Fault Sensitivity Analysis," "Wear-out simulation of VLSI systems," Center for Reliable and High-

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Fault-sensitivity and wear-out analysis of VLSI sensitivity. [Gwan Seung Choi; Gwan Seung Choi; United States. National Aeronautics and Space Administration.

### **Design for manufacturing meets advanced process**

tend to have gradual process changes as consumables wear out Very Large Scale Integration (VLSI) Systems Design for manufacturing meets advanced

### **Fault tree analysis as a tool for safety**

Consideration of sensitivity Fault tree analysis is a This indicates that the quantitative evaluation is not applicable to the "break-in" and "wear-out

### **Air force - welcome to at&l**

An offeror needs to document their Phase I or II proposal's over existing SAR systems. Show by analysis how this goal Choi, G. W. Turner, and S. J

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