

**PVD For Microelectronics: Sputter Deposition Applied
To Semiconductor Manufacturing (Thin Films) By
Ronald A. Powell; Stephen Rosnagel**

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The term physical vapor deposition originally appeared in the 1966 book Vapor Deposition by C. F. Sponner. Sputter deposition: including semiconductor devices,

Handbook of chemical vapor deposition (cvd)

Handbook of Chemical Vapor Deposition the field of semiconductor and microelectronics where most Vapor Deposition of Inorganic Thin Films.

Patent us7067421 - multilevel copper interconnect

Structures and methods provide multilevel wiring interconnects in an integrated circuit assembly which alleviate problems associated with integrated circuit size and

Brevetto us7262130 - methods for making

Accordingly, the inventors devised one or more exemplary methods for making integrated-circuit wiring from materials, such as copper-, silver-, and gold-based metals.

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Patent us5628659 - method of making a field

A system and method is available for fabricating a field emitter device, where in an emitter material, such as copper, is deposited over a resistive layer which has

Handbook of chemical vapor desposition cvd

3 Sputtering Techniques ..0 PHYSICAL VAPOR DEPOSITION This is particularly true in the field of semiconductor and microelectronics CVD/PVD thin films

Patent us7262505 - selective electroless-plated

Structures and methods are provided which include a selective electroless copper metallization. The present invention includes a novel methodology for forming copper

Chapter 9 pvd materials and processes

that are utilized in physical vapor desposition Sputter Desposition Applied to Semiconductor Manufacturing. Edited By Ronald A. Powell and Stephen M. Rossnagel.

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Patent us7262130 - methods for making

Accordingly, the inventors devised one or more exemplary methods for making integrated-circuit wiring from materials, such as copper-, silver-, and gold-based metals.

Chapter 3 plasma systems

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- viniti

Low-Pressure Plasma After-Treatment of Pollutants Emitted During Semiconductor Manufacturing Deposition of ZnO Thin Films by physical vapor deposition

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Patent us5969422 - plated copper interconnect

A high conductivity interconnect structure is formed by electroplating or electroless plating of Cu or a Cu-base alloy on a seed layer comprising an alloy of a

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Patent us7763545 - semiconductor device

In a semiconductor device manufacturing method etching of a conductive film that acts as an electrode of the capacitor formed over a semiconductor

Spts - metal deposition equipment for

PVD; Semiconductor. LPCVD technologies covering conventional PVD through to MOCVD . Metal Deposition Technologies. Standard PVD conventional sputter modules

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